

## Inventor Name Search Result

Your Search was:

Last Name = THURSTON

First Name = ANDREW

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">60211851</a>	Not Issued	159	06/15/2000	METHOD AND APPARATUS FOR DETECTING ERRORS IN A BACKPLANE FRAME	THURSTON, ANDREW J.
<a href="#">60211834</a>	Not Issued	159	06/15/2000	CHANNEL ORDERING FOR COMMUNICATION SIGNALS SPLIT FOR MATRIX SWITCHING	THURSTON, ANDREW J.
<a href="#">60211830</a>	Not Issued	159	06/15/2000	CONCATENATION DETECTION ACROSS MULTIPLE CHIPS	THURSTON, ANDREW J.
<a href="#">60211559</a>	Not Issued	159	06/15/2000	PATH AIS INSERTION FOR CONCATENATED PAYLOADS ACROSS MULTIPLE PROCESSORS	THURSTON, ANDREW J.
<a href="#">10960836</a>	Not Issued	020	10/07/2004	SYSTEM AND METHOD FOR IMPLEMENTING A REED SOLOMON MULTIPLICATION SECTION FROM EXCLUSIVE-OR LOGIC	THURSTON, ANDREW J.
<a href="#">10433921</a>	Not Issued	030	06/06/2003	SIGNAL PROCESSING	THURSTON, ANDREW
<a href="#">10432857</a>	Not Issued	030	05/28/2003	SIGNAL LEVEL CONTROL	THURSTON, ANDREW
<a href="#">10432318</a>	Not Issued	020	05/21/2003	RAKE RECEIVER AND A METHOD OF PROVIDING A FREQUENCY ERROR ESTIMATE	THURSTON, ANDREW
<a href="#">10416297</a>	Not Issued	030	05/08/2003	TIME ALIGNMENT IN A CDMA SYSTEM	THURSTON, ANDREW
<a href="#">09822950</a>	Not Issued	041	03/30/2001	BCH FORWARD ERROR CORRECTION DECODER	THURSTON, ANDREW J.
<a href="#">09822733</a>	Not Issued	095	03/30/2001	GALOIS FIELD MULTIPLY ACCUMULATOR	THURSTON, ANDREW J.
<a href="#">09822713</a>	Not Issued	080	03/30/2001	AUTOMATIC GENERATION OF HARDWARE DESCRIPTION LANGUAGE CODE FOR COMPLEX POLYNOMIAL FUNCTIONS	THURSTON, ANDREW J.
<a href="#">09821948</a>	Not Issued	094	03/30/2001	ERROR INSERTION CIRCUIT FOR SONET FORWARD ERROR CORRECTION	THURSTON, ANDREW J.
<a href="#">09789849</a>	Not Issued	071	02/21/2001	NON-STANDARD CONCATENATION MAPPING FOR PAYLOADS	THURSTON, ANDREW J.
<a href="#">09746895</a>	<a href="#">6684350</a>	150	12/22/2000	REPETITIVE PATTERN TESTING	THURSTON, ANDREW J.

				CIRCUIT FOR AC-COUPLED SYSTEMS	
<a href="#">09671965</a>	<a href="#">6738392</a>	150	09/27/2000	METHOD AND APPARATUS OF FRAMING HIGH-SPEED SIGNALS	THURSTON, ANDREW J.
<a href="#">09629474</a>	<a href="#">6934305</a>	150	07/31/2000	METHOD AND APPARATUS FOR DETECTING ERRORSIN A BACKPLANE FRAME	THURSTON, ANDREW J.
<a href="#">09608461</a>	<a href="#">6801548</a>	150	06/30/2000	CHANNEL ORDERING FOR COMMUNICATION SIGNALS SPLIT FOR MATRIX SWITCHING	THURSTON, ANDREW J.
<a href="#">09608097</a>	<a href="#">6735197</a>	150	06/30/2000	CONCATENATION DETECTION ACROSS MULTIPLE CHIPS	THURSTON, ANDREW J.
<a href="#">09607912</a>	Not Issued	094	06/30/2000	PATH AIS INSERTION FOR CONCATENATED PAYLOADS ACROSS MULTIPLE PROCESSORS	THURSTON, ANDREW J.
<a href="#">08313316</a>	<a href="#">5668552</a>	150	12/28/1994	ANALOGUE-TO-DIGITAL CONVERTERS	THURSTON, ANDREW M.
<a href="#">08302039</a>	<a href="#">5585801</a>	150	09/09/1994	ANALOGUE-TO-DIGITAL CONVERTERS AND DIGITAL MODULATORS	THURSTON, ANDREW M.
<a href="#">07976364</a>	Not Issued	161	11/18/1992	ANALOGUE-TO-DIGITAL CONVERTER	THURSTON, ANDREW M.
<a href="#">07886588</a>	<a href="#">5392039</a>	250	05/21/1992	SIGMA-DELAT ANALOG TO DIGITAL CONVERTER	THURSTON, ANDREW M.
<a href="#">07528977</a>	<a href="#">5027120</a>	150	05/25/1990	DELTA-SIGMA CONVERTER WITH BANDPASS FILTER FOR NOISE REDUCTION IN RECEIVERS	THURSTON, ANDREW M.

Inventor Search Completed: No Records to Display.

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THURSTON	ANDREW

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